

REAL TIME CLOCK MODULE (I2C)

For Automotive, Built-in 32.768 kHz DTCXO,







Product Number (2,000 pcs / Reel)

RA8000CE YB A0: X1B000501A00115 RA8000CE YB B8: X1B000501A00915 RA8000CE YB C0: X1B000501A01015

RA8000CE

High Stability, +125 °C

• Built in frequency adjusted 32.768 kHz crystal unit and DTCXO

• Interface Type : I²C-Bus

• Time stamp function : 2 times stamped from year to second

• Reset functions with a delay : Detect a main power supply and remove the reset

Interrupt output
 Alarm interruption
 Wake up every minute or every second
 Day, date, hour, minute, second

Auto repeat wakeup timer interruption

• Self-monitoring interruption : Crystal oscillation stop, V_{DD} low

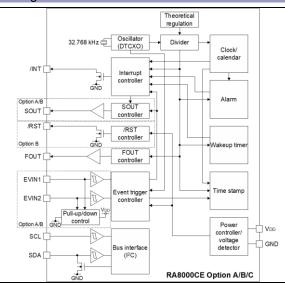
AEC-Q100 compliant



RA8000CE

 $(3.2 \times 2.5 \text{ mm}, t = 1.0 \text{ mm Max.})$

Block diagram



Overview

Interface type

I²C-Bus interface Fast-Mode 400 kHz

High stability

YB: ±5.0 x 10⁻⁶ / -40 °C to +85 °C (Monthly rate: ±13.2 seconds) : ±8.0 x 10⁻⁶ / +85 °C to +105 °C (Monthly rate: ±21 seconds) : ±50.0 x 10⁻⁶ / +105 °C to +125 °C (Monthly rate: ±132 seconds)

Clock output function

Output frequency is selectable from 32.768 kHz, 1024 Hz, 1 Hz

Wakeup timer function

Can generate an interrupt in 976.56 µs to 32-year cycle.

Can be used as a time integration meter.

Can be used as a watchdog timer.

Time stamp function

Record data: 1/1024 seconds to 1 second, seconds, minutes, hours, days, months, years.

Number of recordable events: 2 events

Trigger source: External event (EVIN) input, voltage drop/oscillation stop status detected, command input from the host.

EVIN pin has function of chattering-cancel.

Reset function with a delay

Can output a reset signal when a VDD voltage drop status is detected.

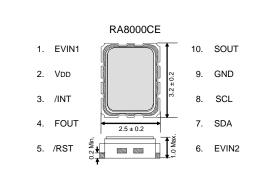
Status output (SOUT)

Can output the selected internal flag (interrupt flag, voltage drop detection flag) status.

Pin Function

Signal Name	1/0	Function			
EVIN1, 2	Input	External event input pins Pull-up and pull-down is configurable by the resisters			
SCL	Input	Serial clock input pin			
SDA	Input / Output	Serial data input and output pin			
FOUT	Output	Frequency output pin (CMOS). 32.768 kHz (default), 1024 Hz or 1 Hz clock output is selectable. This pin can be switched to the wakeup timer interrupt output (CMOS)			
/INT	Output	Interrupt output pin (N-ch. open drain). The wakeup timer, time update, alarm, and/or event detection interrupt signals can be selected to output from this pin. When two or more signals are selected, they are NORed before being output.			
/RST	Output	Reset output pin (N-ch. open drain)			
SOUT	Output	Status output pin			
VDD	-	Power-supply pin			
GND	-	Ground pin			

Terminal connection / External dimensions (Unit: mm)

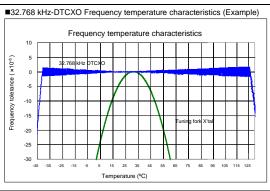


*The above diagram is the terminal layout for Option B. For other options, please refer to the Pin Option section.

Specifications (characteristics)

■ Electrical Characteristics									
Item	Symbol Conditions			Min.	Тур.	Max.	Unit		
Operating voltage	VDD	D -				1.6	3.0	5.5	V
Temp. compensated Voltage	Vтем	-			1.6	3.0	5.5	V	
Clock supply voltage	Vclk	-			1.3	3.0	5.5	V	
Operating temperature	Ta	-		-40	+25	+125	°C		
	Δf/f	YB	$T_a = -40$ °C to +85 °C		±5.0				
Frequency tolerance			Ta = +85 °C to +105 °C		±8.0			x 10 ⁻⁶	
			T _a = +105 °C to +125 °C		±50.0				
	I _{DD1}	/INT = Hi-Z, FOUT: Output OFF (Hi-Z), Temp. Compensation interval 2.0 s, SCL = SDA = H		No /RST pin	$V_{DD} = 5 V$	1	0.35	1.8	μΑ
Current consumption	I _{DD2}				V _{DD} = 3 V	-	0.3	1.7	
	IDD11			With	V _{DD} = 5 V	-	1.5	3.7	
	I _{DD12}			/RST pin	$V_{DD} = 2 V$	-	0.6	2.25	

* Refer to application manual for details



Pin Option

Pin No.	Pin name						
Pin No.	Option A	ption A Option B Op					
1	EV	N.C.					
2	Vdd						
3	/INT						
4	FOUT						
5	N.C. /RST N.		N.C.				
6	EV	N.C.					
7	SDA						
8	SCL						
9	GND						
10	SO	N.C.					



Product name

RA8000CE YB A 0 1) 2 34

① Model CE type package 3.2 x 2.5 x 1.0 mm

Option C

2 Frequency tolerance

YB: $\pm 5.0 \times 10^{-6}$ / $\pm 40 \,^{\circ}$ C to $\pm 85 \,^{\circ}$ C (Monthly rate: $\pm 13.2 \text{ seconds}$) $\pm 8.0 \times 10^{-6}$ / $\pm 85 \,^{\circ}$ C to $\pm 105 \,^{\circ}$ C (Monthly rate: $\pm 21 \text{ seconds}$) $\pm 50.0 \times 10^{-6}$ / $\pm 105 \,^{\circ}$ C to $\pm 125 \,^{\circ}$ C (Monthly rate: $\pm 132 \text{ seconds}$)

- 3 Pin Option
 - A: Option A
 - B: Option B
 - C: Option C
- 4 Reset output function
 - 0: No /RST pin
 - 8: With /RST pin (VDD drop detection voltage: +2.4 V Typ.)

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At Seiko Epson, all environmental initiatives operate under the Plan-Do-Check-Action (PDCA) cycle designed to achieve continuous improvements. The environmental management system (EMS) operates under the ISO 14001 environmental management standard.

All of our major manufacturing and non-manufacturing sites, in Japan and overseas, completed the acquisition of ISO 14001 certification.

ISO 14000 is an international standard for environmental management that was established by the International Standards Organization in 1996 against the background of growing concern regarding global warming, destruction of the ozone layer, and global deforestation.

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► Explanation of the mark that are using it for the catalog



►Pb free.



► Complies with EU RoHS directive.

*About the products without the Pb-free mark.

Contains Pb in products exempted by EU RoHS directive.

(Contains Pb in sealing glass, high melting temperature type solder or other.)



▶ Designed for automotive applications such as Car Multimedia, Body Electronics, Remote Keyless Entry etc.



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